

WHAT IS CLAIMED IS:

1. A method for processing multimedia data, comprising:

indexing the multimedia data to an i by j matrix;

storing the i by j matrix in a data storage device utilizing odd/even index sequencing of the i by j matrix;

retrieving data from the data storage device; and

reconstructing the i by j matrix utilizing odd/even index sequencing of retrieved data.
2. The method of claim 1 wherein the multimedia data is selected from still image data and video image data.
3. The method of claim 1, further comprising disabling a data recovery procedure programmed on the data storage device.
4. The method of claim 1 wherein the multimedia data represents an image having i times j pixels.
5. The method of claim 1 wherein the multimedia data represents an image having i times j subimages and wherein the i by j matrix corresponds to the i times j subimages.
6. The method of claim 5, further comprising:

compressing the subimages before storing the i by j matrix in the data storage device; and

decompressing the reconstructed i by j matrix to render the image.
7. The method of claim 1 wherein the odd/even index sequencing comprises an odd/odd index sequence, an odd/even index sequence, an even/odd index sequence, and an even/even index sequence.
8. The method of claim 7 wherein the index sequences are stored in logic blocks in the data storage device.

9. The method of claim 7 wherein each index sequence is stored in one or more logic blocks in the data storage device.

10. The method of claim 9, further comprising, when a logic block is flawed, assigning one or more fixed values for one or more portions of the index sequences contained in the flawed logic block.

11. The method of claim 9, further comprising, when a logic block is flawed, interpolating one or more replacement values for one or more portions of the index sequences contained in the flawed logic block.

12. A signal bearing medium, comprising a program which, when executed by a processor, performs a method comprising:

indexing the multimedia data to an i by j matrix;

storing the i by j matrix in a data storage device utilizing odd/even index sequencing of the i by j matrix;

retrieving data from the data storage device; and

reconstructing the i by j matrix utilizing odd/even index sequencing of retrieved data.

13. The signal bearing medium of claim 12, wherein the method further comprises disabling a data recovery procedure programmed on the data storage device.

14. The signal bearing medium of claim 12 wherein the multimedia data represents an image having i times j subimages and wherein the i by j matrix corresponds to the i times j subimages.

15. The signal bearing medium of claim 14, wherein the method further comprises:

compressing the subimages before storing the i by j matrix in the data storage device; and

decompressing the reconstructed i by j matrix to render the image.

16. The signal bearing medium of claim 12 wherein the odd/even index sequencing comprises an odd/odd index sequence, an odd/even index sequence, an even/odd

17. The signal bearing medium of claim 16 wherein each index sequence is stored in one or more logic blocks in the data storage device.

18. The signal bearing medium of claim 17 wherein the method further comprises, when a logic block is flawed, interpolating one or more replacement values for one or more portions of the index sequences contained in the flawed logic block.

a processor;

one or more storage devices for storing multimedia data connected to the processor, wherein the processor is configured to perform a method for processing multimedia data, comprising:

storing the i by j matrix in a data storage device utilizing odd/even index sequencing of the i by j matrix;

reconstructing the i by j matrix utilizing odd/even index sequencing of retrieved data.

20. The system of claim 19 wherein the processor is further configured to disable a data recovery procedure programmed on the data storage device.

21. The system of claim 19 wherein the odd/even index sequencing comprises an odd/odd index sequence, an odd/even index sequence, an even/odd index sequence, and an even/even index sequence.

22. The system of claim 21 wherein the processor is further configured to store each index sequence is stored in one or more logic blocks in the data storage device.

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one or more replacement values, when a logic block is flawed, for one or more portions of the index sequences contained in the flawed logic block.

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